

The **LT[®]1057** is now available in an S0-8 package for both commercial and industrial temperature ranges. For complete specifications, typical performance characteristics and applications information, please see the **LT1057** data sheet.

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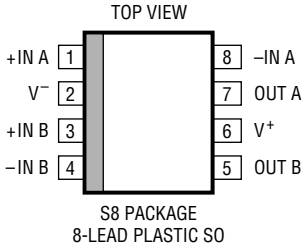
ABSOLUTE MAXIMUM RATINGS

Operating Temperature Range

LT1057CS8..... $0^{\circ}\text{C} \leq T_A \leq 70^{\circ}\text{C}$

LT1057IS8..... $-40^{\circ}\text{C} \leq T_A \leq 85^{\circ}\text{C}$

PACKAGE/ORDER INFORMATION

 <p>TOP VIEW</p> <p>S8 PACKAGE 8-LEAD PLASTIC SO</p>	ORDER PART NUMBER
	LT1057CS8 LT1057IS8
	S8 PART MARKING
	1057 1057I

Please note that the LT1057CS8/LT1057IS8 standard surface mount pinout differs from that of the LT1057 standard CERDIP or PDIP packages.

ELECTRICAL CHARACTERISTICS $V_S = 15\text{V}$ unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
V_{OS}	Input Offset Voltage	$T_A = 25^{\circ}\text{C}$ $0^{\circ}\text{C} \leq T_A \leq 70^{\circ}\text{C}$ $-40^{\circ}\text{C} \leq T_A \leq 85^{\circ}\text{C}$		220 400 500	1200 1900 2300	μV μV μV
$\Delta V_{OS}/\Delta T$	Input Offset Voltage Drift (Note 4)	$0^{\circ}\text{C} \leq T_A \leq 70^{\circ}\text{C}$ $-40^{\circ}\text{C} \leq T_A \leq 85^{\circ}\text{C}$		4.0 4.5	16 16	$\mu\text{V}/^{\circ}\text{C}$ $\mu\text{V}/^{\circ}\text{C}$
I_B	Input Bias Current	$-40^{\circ}\text{C} \leq T_A \leq 85^{\circ}\text{C}$		± 100	± 900	pA
I_{OS}	Input Offset Current	$-40^{\circ}\text{C} \leq T_A \leq 85^{\circ}\text{C}$		35	600	pA

Note 4: Not 100% production tested.

For further information regarding this specification notice contact:

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